



K-No.: 26622

300mA Differential Current Sensor for 5V Supply Voltage

For the electronic measurement of current:
DC, AC, pulsed, with galvanic isolation between
the primary and the secondary circuit

Date: 02.02.2022

Customer: Standard type

Customers Part no:

Page 1 of 4

Description	Characteristics	Applications
<ul style="list-style-type: none"> Closed loop (compensation) Current Sensor with magnetic probe Printed circuit board mounting Casing and materials UL-listed 	<ul style="list-style-type: none"> excellent accuracy very low offset current very low temperature dependency and offset drift very low hysteresis of offset current short response time wide frequency bandwidth compact design reduced offset ripple 	Mainly used for stationary operation in industrial applications: <ul style="list-style-type: none"> Solar inverter

Electrical data - Ratings

I _{PN}	Primary nominal RMS current	50	A
I _{ΔN}	Differential rated RMS current	0.3	A
V _{OUT}	Output voltage @ I _{ΔP}	V _{REF} ± (0.74 * I _{ΔP} / I _{ΔN})	V
V _{OUT(0)} ¹	Output voltage @ I _P =0A, θ _A =25°C	V _{REF} ± 0.025	V
V _{OUT(Error)}	in case of error (current sensor) V _{OUT} < 0.5V is set	< 0.5	V
V _{REF}	internal reference voltage	2.5 ± 0.005	V
	external reference voltage range	1.4 ... 3.5	V
V _{REF(test current)} ²	Reference voltage (external)	0 ... 0.1	V
V _{OUT(test current)} ²	Output voltage @ V _{REF} = 0 ... 0.1V	V _{OUT(0)} + 0.25 ± 0.06	V
K _N	Transformation ratio	1:1:1:1 : 20 : 1000	

¹ with switching on and after "test current" the sensor is degaussed by an internal AC-current for about 110ms.

In this time the output is set to V_{OUT} < 0.5V.

² If V_{REF} is set external to 0...0.1V an internal test current is generated.

	Accuracy – Dynamic performance data	min.	typ.	max.	Unit
I _{ΔP,max}	Max. measuring range (differential current)	±0.85			A
X	Accuracy @ I _{ΔN} , θ _A = 25°C		±1.5		%
ε _L	Linearity		±1		%
V _O (V _{OUT} -V _{REF})	Offset voltage @ I _P = 0A, θ _A = 25°C			±25	mV
ΔV _O /ΔT	Temperature drift of V _{OUT} @ I _P =0A, θ _A		0.1		mV/°C
t _r	Response time @ 90% of I _{ΔN}		35		μs
f _{BW}	Frequency bandwidth	DC...8			kHz

General data

θ _A	Ambient operation temperature	-40	85	°C
θ _S	Ambient storage temperature (acc. to M3101)	-40	85	°C
m	Mass		75	g
V _c	Supply voltage	4.75	5	5.25
I _c	Supply current at I _P = 0A and RT		15	mA

1) _S clear	Clearance (component without solder pad)	8.5		mm
1) _S creep	Creepage (component without solder pad)	10.0		mm
1) _U sys	System voltage *determines impulse voltage acc. table 7		600	V _{RMS}
1) _U AC	Working voltage *acc. table 10		1000	V _{RMS}
1) _U PD	Rated discharge voltage *acc. table 24 with U _{PD} =U _{AC} *√2		1414	V _{PEAK}

¹⁾Constructed and manufactured and tested in accordance with IEC 61800-5-1:2007
Reinforced Insulation, Pollution degree 2, Overvoltage category III, Insulation material group I

Date	Name	Issue	Amendment				
02.02.2022	NSch.	81	Applicable documents changed on sheet 2. The color of the plastic material... added. Minor change				
Hrg.: R&D-PD NPI D editor	Bearb.: DJ designer		MC-PM: NSch. check			freig.: SB released	



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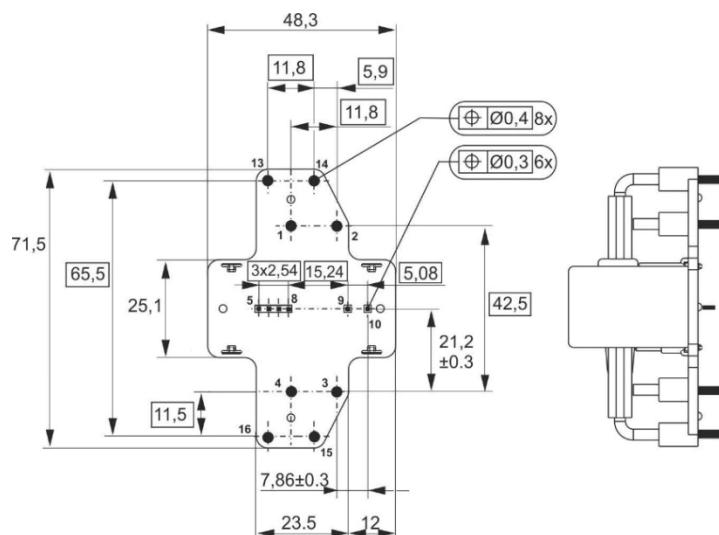
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Page 2 of 4

Mechanical outline (mm):

General tolerances DIN ISO 2768-c





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Page 3 of 4

Electrical data: (investigate by a type checking)		min.	typ.	max.	Unit	
$V_{C,max}$	maximum supply voltage (without function)			6	V	
I_c	Supply current with primary current			$15mA + I_{\Delta P} * K_N + V_{OUT}/R_L$	mA	
$I_{OUT,SC}$	Short circuit output current			± 10	mA	
R_s	Secondary coil resistance @ $\theta_A = 85^\circ C$			80	Ω	
R_{Test}	Test winding resistance @ $\theta_A = 25^\circ C$			0.9	Ω	
$R_{P1,P2}$	Primary wire resistance @ $\theta_A = 25^\circ C$			0.24	$m\Omega$	
$R_{i,REF}$	Internal resistance of reference input			470	Ω	
$R_{i,OUT}$	Output resistance of V_{OUT}			470	Ω	
$\Delta X_e/\Delta \theta$	Temperature drift of $X @ \vartheta_A = -40^\circ C \dots 85^\circ C$			400	ppm/K	
$\Delta V_{REF}/\Delta \theta$	Temperature drift of $V_{REF} @ \vartheta_A = -40^\circ C \dots 85^\circ C$			5	50	ppm/K
$\Delta V_o = \Delta(V_{OUT}-V_{REF})$	Sum of any offset drift including:			32	mV	
V_{ot}	Long term drift of V_o			12	mV	
V_{OT}	Temperature drift of $V_o @ \vartheta_A = -40^\circ C \dots 85^\circ C$			10	mV	
$\Delta V_o/\Delta V_c$	Supply voltage rejection ratio			10	mV/V	
V_{OH}	Hysteresis of $V_{OUT} @ I_P = 0$ (after an overload of $1000x I_{\Delta N}$)			75	125	mV
$V_{OH, Demag}$	Hysteresis after Degaussing				25	mV
V_{OSS}	Offsetripple (without external filter)			70	mV	
V_{OSS}	Offsetripple (with 20 kHz-Filter, first order)			20	mV	
V_{OSS}	Offsetripple (with 1 kHz-Filter, first order)			6	mV	
	Mechanical stress according to M3209/3 Settings: 10-2000Hz, 1min/Octave, 2 hours			1.5	g	

Routine Tests: (Measurement after temperature balance of the samples at room temperature, SC=significant characteristic)

V_{OUT} (SC)	(100%) M3011/6:	Output voltage vs. reference	729 ... 751	mV
V_o	(100%) M3226:	Offset voltage ($V_{OUT}-V_{REF}$)	± 25	mV
V_{OUT} (test current)	(100%)	Output voltage @ $V_{REF} = 0V$	250 ± 60	mV
U_d	(100%) M3014:	Test voltage, 1s, *acc. table 21	1.8	kV_{RMS}
$U_{PD, PDE}$ $U_{PD} \cdot 1.875$	(AQL 1/S4)	Partial discharge voltage (extinction) *acc. table 24	1.5 1.875	kV_{RMS}

Type Tests: (Precondition acc. to M3236)

\hat{U}_w	M3064:	Impulse test (1.2 μ s/50 μ s wave form) Pin 1-4 vs. Pin 5-10	6	kV
$\hat{U}_{W, prim-prim}$	M3064:	Impulse test (1.2 μ s/50 μ s wave form) Pin 1 vs. Pin 13,14 and Pin 14 vs. Pin 1,2	6	kV
U_d	M3014:	Test voltage, 60s Pin 1-4 vs. Pin 5-10	3.6	kV_{RMS}
$U_{d, prim-prim}$	M3014:	Test voltage between primary conductors, 60s Pin 1 vs. Pin 13,14 and Pin 14 vs. Pin 1,2	3.6	kV_{RMS}
$U_{PD, PDE}$ $U_{PD} \cdot 1.875$		Partial discharge voltage (extinction) *acc. table 24	1.5 1.875	kV_{RMS}

* IEC 61800-5-1:2007

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Page 4 of 4

Explanation of several terms used in the tables:

V_{ot} Long term drift of V_O after 100 temperature cycles in the range -40°C to 85°C.

t_r Response time, measured as a delay time at I_{ΔP} = 0.9 * I_{ΔN} between a rectangular primary current and the output current or voltage.

t_{ra} Reaction time, measured as a delay time at I_{ΔP} = 0.1 * I_{ΔN} between a rectangular primary current and the output current or voltage.

X_{ges(I_{ΔN})} The sum of all possible errors over the temperature range by measuring a current I_{ΔN}:

$$X_{\text{ges}(I_{\Delta N})} = 100 * \left| \frac{V_{\text{OUT}}(I_{\Delta N}) - 2.5V}{0.74V} - 1 \right| \%$$

X Permissible measurement error in the final inspection at RT, defined by

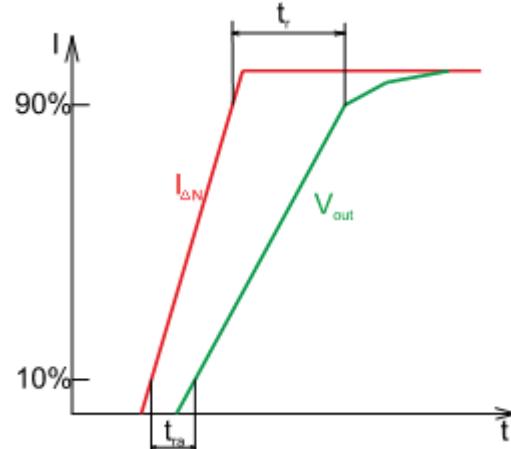
$$X = 100 * \left| \frac{V_{\text{OUT}}(I_{\Delta N}) - V_{\text{OUT}}(0)}{0.74V} - 1 \right| \%$$

ΔX_θ ΔX_θ = X_{θmax} - X_{θmin}

ε_L Linearity fault defined by: ε_L = 100 * $\left| \frac{I_{\Delta P}}{I_{\Delta N}} - \frac{V_{\text{OUT}}(I_{\Delta P}) - V_{\text{OUT}}(0)}{V_{\text{OUT}}(I_{\Delta N}) - V_{\text{OUT}}(0)} \right| \%$

Where I_{ΔP} is any input DC current and V_{OUT} the corresponding output term. (V_O = 0).

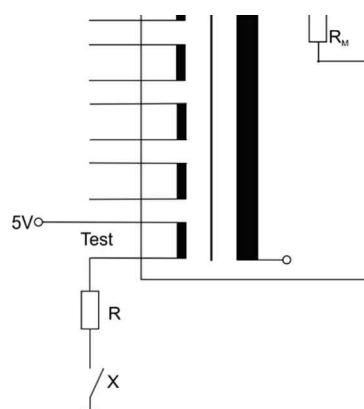
RT Room temperature



Application Information

The external test current can be generated with the use of a resistor R and a switch X or something similar (Transistor, Mosfet, etc.). The resistor determine the current at a given voltage and so the output voltage can be calculated.

$$V_{\text{OUT}} = V_{\text{REF}} \pm \frac{0.74 \cdot \frac{5V}{R + R_{\text{Test}}} \cdot 20}{I_{\Delta N}}$$

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